UNITED STATES PATENT AND TRADEMARK OFFICE **CERTIFICATE OF CORRECTION**

PATENT NO.

: 6,980,147 B2

Page 1 of 1

APPLICATION NO.: 10/820168

: December 27, 2005

INVENTOR(S)

: Ronald F. Mathis et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

- 1. Column 4, line 50; "wherein the M-band filter bank" should read: --wherein the M-band filters in said M-band filter bank--
- 2. Column 4, line 59; "The high-speed" should be -- A high-speed--.
- 3. Column 4, line 61, after processor, change "compriding" to --comprising--
- 4. Column 4, line 65, after "a", insert --a digitizer for--
- 5. Column 6. Line 10, after "technology.", delete "that of an M-band filter"

Signed and Sealed this

Twenty-first Day of November, 2006

JON W. DUDAS Director of the United States Patent and Trademark Office